ABSTRACT OF THE DISCLOSURE

Spreading section 101 spreads each transmission signal using a spreading code specific to the signal. Spreading section 102 spreads a known signal using a spreading code specific to the signal. S/P converter 104 breaks down a signal on which the spread transmission signals and the known signal are multiplexed for every chip and IFFT section 105 subjects the chip data string to frequency division multiplexing. FFT section 107 carries out Fourier transform processing on the reception signal and extracts each subcarrier signal. P/S converter 109 converts a plurality of parallel signals subjected to compensation processing to a single serial signal. Despreading section 110 despreads the reception signal converted to a serial signal using a predetermined spreading code. Despreading section 111 despreads the reception signal converted to a serial signal using a known signal spreading code. Residual phase error detection section 113 detects a residual phase error using the known signal and received known signal subjected to despreading processing. Phase compensation section 112 compensates each reception signal based on the residual phase error.